## 2SK3380

## Silicon N Channel MOS FET High Speed Switching <br> HITACHI

## Features

- Low on-resistance

$$
\begin{aligned}
& \mathrm{R}_{\mathrm{DS}}=1.26 \Omega \text { typ. }\left(\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=150 \mathrm{~mA}\right) \\
& \mathrm{R}_{\mathrm{DS}}=2.8 \Omega \text { typ. }\left(\mathrm{V}_{\mathrm{GS}}=4 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=50 \mathrm{~mA}\right)
\end{aligned}
$$

- 4 V gate drive device.


## Outline



Absolute Maximum Ratings $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Item | Symbol | Ratings | Unit |
| :--- | :--- | :--- | :--- |
| Drain to source voltage | $\mathrm{V}_{\mathrm{DSs}}$ | 30 | V |
| Gate to source voltage | $\mathrm{V}_{\mathrm{GSs}}$ | $\pm 20$ | V |
| Drain current | $\mathrm{I}_{\mathrm{D}}$ | 300 | mA |
| Drain peak current | $\mathrm{I}_{\mathrm{D} \text { (pulse) }}$ Note1 | 1.2 | A |
| Body-drain diode reverse drain current | $\mathrm{I}_{\mathrm{DR}}$ | 300 | mA |
| Channel dissipation | Pch | Tch | 300 |
| Channel temperature | Tstg | 150 | mW |
| Storage temperature | ${ }^{\circ} \mathrm{C}$ |  |  |

Note: 1. PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$

Electrical Characteristics $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Item | Symbol | Min | Typ | Max | Unit | Test Conditions |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| Drain to source breakdown <br> voltage | $\mathrm{V}_{(\mathrm{BR}) \mathrm{DSS}}$ | 30 | - | - | V | $\mathrm{I}_{\mathrm{D}}=100 \mu \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0$ |

Note: 2. Pulse test
See characteristics curves of 2SK3288

## Main Characteristics



## Package Dimensions



| Hitachi Code | SPAK |
| :---: | :---: |
| EIAJ | - |
| JEDEC | - |

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